

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Withdrawn) A digital picture capturing device comprising:
a plurality of pixel elements arranged in an array and adapted to capture picture information; and
at least one encryption logic adapted to encrypt picture information from at least a first portion of the pixel elements with a first encryption key and to encrypt picture information from at least a second portion of the pixel elements with a second encryption key.
2. (Withdrawn) The digital picture capturing device according to claim 1, wherein:
the first portion is a first single pixel element; and
the second portion is a second single pixel element;
wherein the encryption logic is adapted to encrypt each of the pixel elements with a different encryption key.
3. (Withdrawn) The digital picture capturing device according to claim 2, wherein:
the encryption logic is a plurality of encryption logics; and
each of the encryption logics connects to a respective one of the pixels.
4. (Withdrawn) The digital picture capturing device according to claim 3, wherein each of the plurality of encryption logics is located at a respective one of the pixels.

5. (Withdrawn) The digital picture capturing device according to claim 1, wherein the first portion is a first plurality of pixels and the second portion is a second plurality of pixels.

6. (Withdrawn) The digital picture capturing device according to claim 5, wherein the encryption logic is a single component that encrypts the first portion with the first encryption key and encrypts the second portion with the second encryption key.

7. (Withdrawn) The digital picture capturing device according to claim 6, further comprising:

pixel logic adapted to receive picture information from the first portion and the second portion and adapted to dispatch the picture information to the encryption logic.

8. (Withdrawn) The digital picture capturing device according to claim 1, wherein the array is a picture gathering device of a digital camera.

9. (Currently Amended) A digital picture display device comprising:
a plurality of pixel elements arranged in an array and adapted to display picture information; and

~~at least one decryption device~~ logic adapted to decrypt picture information for at least a first portion of the pixel elements with a first decryption key and to decrypt picture information for at least a second portion of the pixel elements with a second decryption key.

10. (Currently Amended) The digital picture display device according to claim 9, wherein:

the first portion is a first single pixel element; and

the second portion is a second single pixel element;

wherein the decryption ~~device~~ logic is adapted to decrypt picture information for each of the pixel elements with a different decryption key.

11. (Currently Amended) The digital picture display device according to claim 10, wherein:

each of the pixel elements includes the decryption device logic ~~is a plurality of decryption devices;~~

~~each of the decryption devices~~ connects to a respective one of the pixels.

12. (Currently Amended) The digital picture display device according to claim 11, wherein ~~each of the decryption devices~~ logic is positioned at a respective one of the pixels.

13. (Original) The digital picture display device according to claim 9, wherein the first portion is a first plurality of pixels and the second portion is a second plurality of pixels.

14. (Currently Amended) The digital picture display device according to claim 13, wherein the decryption device logic is a single component that decrypts the first portion with the first decryption key and decrypts the second portion with the second decryption key.

15. (Currently Amended) The digital picture display device according to claim 14, further comprising:

pixel logic adapted to receive decrypted picture information for the first portion from the decryption device logic and to receive decrypted picture information for the second portion from the decryption device logic and to dispatch the picture information for the first portion of pixel elements to the decryption device logic and to dispatch the picture information for the second portion to the second portion of pixel elements to the decryption device logic.

16. (Original) The digital picture display device according to claim 9, wherein the array is a picture gathering device of a digital camera.

17. (Withdrawn) A method for encrypting information from an array, comprising:
providing a plurality of pixels arranged in an array and adapted to capture picture information;
partitioning the plurality of pixels into a plurality of portions;
encrypting each of the plurality of portions with a respective one of a plurality of encryption keys, wherein each of the plurality of encryption keys is different than a remainder of the plurality of encryption keys.

18. (Withdrawn) The method according to claim 17, wherein each of the plurality of portions includes a plurality of pixels.

19. (Withdrawn) The method according to claim 17, wherein each of the plurality of portions includes only one pixel.

20. (Original) A method for receiving encrypted picture information, comprising:
providing a display device having a plurality of pixels organized into an array and adapted to display picture information;
receiving a data stream of digital picture information;
decrypting the data stream of digital picture information with a plurality of the encryption keys to obtain a plurality of portions of picture information for each respective one of the plurality of encryption keys; and
dispatching each of the plurality of portions to respective ones of a plurality of different pixel groups in the array.

21. (Original) The method according to claim 20, wherein each of the different pixel groups comprises a plurality of pixels

22. (Original) The method according to claim 20, wherein each of the plurality of pixel groups includes only one pixel.

23. (Original) A micro electronic device, comprising:

an array of display pixels collectively configured to display visible images; and

a plurality of decryption logic components, each decryption logic component associated with different group of the display pixels and configured to decrypt video data directed to the respective group of display pixels.

24. (Original) The micro electronic device of claim 23, wherein each group of display pixels comprises a single display pixel.

25. (Original) A display device, comprising:

a means for displaying visible images from digital data, wherein said display means is segmentable into a plurality of portions; and

a decryption means for decrypting encrypted digital data directed to said portions using a plurality of decryption keys, wherein each portion is associated with a different decryption key.

26. (New) The digital picture display device according to claim 9, wherein the decryption logic is a physical circuit located at each pixel element.

27. (New) The digital picture display device according to claim 9, wherein the decryption logic is a physical circuit directly connected to each pixel element.

28. (New) The digital picture display device according to claim 9, wherein the decryption logic is a physical circuit virtually connected to each pixel element.